

Name	Description
VCC	3.3V.
GPIO 13	Also used for SPI MOSI.
GPIO 12	Also used for SPI MISO.
GPIO 14	Also used for SPI Clock.
GPIO 16	
CH_PD	Chip enable. Should be high for normal operation. <ul style="list-style-type: none"> • 0 – Disabled • 1 – Enabled
ADC	Analog to digital input
REST	External reset. <ul style="list-style-type: none"> • 0 – Reset • 1 – Normal
TXD	UART 0 transmit.
RXD	UART 0 Receive.
GPIO 4	Regular GPIO.
GPIO 5	Regular GPIO.
GPIO 0	Should be high on boot, low for flash update.
GPIO 2	Should be high on boot.
GPIO 15	Should be low on boot and flash.
GND	Ground.